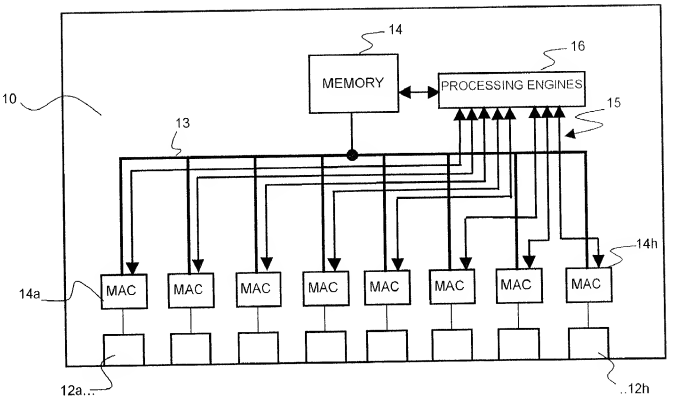
14
16
MEMORY
PROCESSING ENGINES
10
15
13
14h
14a
MAC
MAC
MAC
MAC
MAC
MAC
MAC
MAC
12a..
..12h

FIG.1

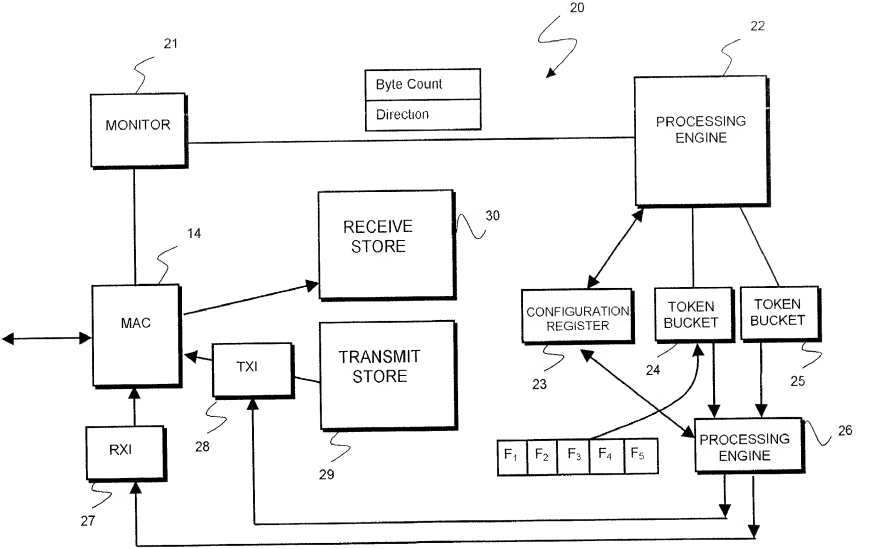
20
21
22
Byte Count
Direction
MONITOR
PROCESSING ENGINE
RECEIVE STORE
30
14
MAC
TXI
TRANSMIT STORE
CONFIGURATION REGISTER
TOKEN BUCKET
TOKEN BUCKET
23
24
25
RXI
28
29
27
F1
F2
F3
F4
F5
PROCESSING ENGINE
26

FIG.2